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In the United States Patent and Trademark Office

Date: April 8, 2002

In re Application of: K. J. Barker, et al.

Serial Number: 09/834,591   *Filed:* 04/13/2001

For: METHOD AND SYSTEM FOR FAST ETHERNET SERIAL PORT MULTIPLEXING TO REDUCE I/O PIN COUNT

Group Art Unit: 2661

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Technology Center 2600

Assistant Commissioner of Patents
Washington, DC 20231

TRANSMITTAL OF FORMAL DRAWINGS

Attached please find formal drawings (3 sheets) for the above-identified application.

Respectfully submitted,

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